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PATENT

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January 9, 2008
Date

Denise Sheridan
Denise Sheridan

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Jigish D. Trivedi

Attorney Docket No.: 501188.04

Patent No. : 7,210,224 B2

Issued : May 1, 2007

Title : METHOD FOR FORMING AN ANTIFUSE

NOTIFICATION OF ERRORS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following errors were noted in a review of the above-identified letters patent. One of these errors occurred in the printing of the patent, while the others were inadvertently made in the original application. Since the errors are of an obvious nature, a formal Certificate of Correction is not believed to be warranted at this time. Therefore, applicant requests that this notification be placed in the Patent and Trademark Office file.

<u>Column, Line</u>	<u>Reads</u>	<u>Should Read</u>
Front page: Abstract (57), Line 10	"over the comers of the"	--over the corners of the--
Column 1, Line 53	"(DRAM) devices can"	--(DRAM) device can--
Column 2, Line 2	"fuses are used typically used"	--fuses are typically used--

Column 4, Line 39

"material formed a"

--material formed from a--

Respectfully submitted,

Date:

Jan. 7, 2008

By:

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Enclosure:

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